JOIN THE WINNING TEAM!

TES Electronic Solutions is a leading international design services company and technology/IP provider. We offer a complete end-to-end service in application-specific electronic product & sub-system design - including supply chain management. We are developing and manufacturing innovative electronic solutions for international customers in all industry segments.

Our comprehensive product development expertise covers a wide range of embedded electronics design disciplines:
- System-architecture and system-design of electronic products
- Radiofrequency, microwave, wireless and antenna design
- PCB based hardware development
- Embedded software design
- HMI and graphics and multimedia systems
- Digital ASICs and FPGAs
- Analog-, Mixed-Signal And RF ASICs

In order to sustain the continuous growth of the company, we are looking for:

Digital ASIC/FPGA Design and Verification Engineers (m/f)
for our locations in Berlin, Düsseldorf, Hamburg, Munich and Stuttgart

We are also looking for mobile engineers, to work embedded in our customers' organisations and/or in a home office.

Your challenge

- Design and verify integrated digital circuits for ASIC and FPGA applications
- Build verification environments and verification components (VIP)
- Prepare verification plans and strategies (functional coverage)
- Understand and verify customer requirements
- Work on a wide range of different applications and sectors like automotive, avionics, signal processing or video applications
- Work in different teams and challenging projects
- Possibility to develop within R&D, verification lead, system architect and/or project management if interested

We are looking for different engineers with following skills:

- Degree in electrical engineering, microelectronics or comparable
- > 2 years professional experience
- Experience with Hardware Description Languages like VHDL and Verilog
- Experience with Verification Description Languages like SystemVerilog, e, SystemC
- Experience in verification methodologies (UVM/OVM)
- Experience in synthesis, place & route, static timing analyses
- FPGA Skills (Xilinx/Altera) are welcome
- Willingness to work temporarily at different sites and in multifarious projects
- Fluent skills in German and English
- Self-motivated and self-organized
- Team player and excellent communication skills

We offer great career opportunities, an attractive salary, flexible working time as well as an excellent working environment. Are you interested? Please send your application to:

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